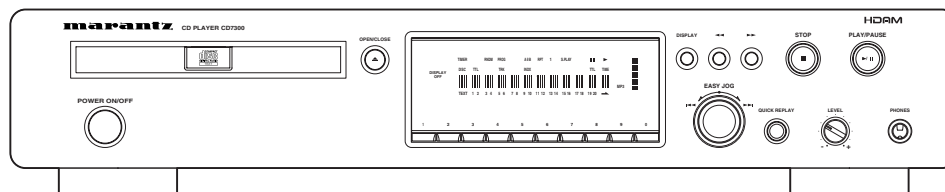


Service Manual

CD7300 /F1N/K1G/S1G/C1G
/N1G/N1B

CD Player

CD7300



HDAM

TABLE OF CONTENTS

SECTION	PAGE
1. TECHNICAL SPECIFICATIONS	1
2. SERVICE PROCEDURE	2
3. SERVICE MODE	6
4. BLOCK DIAGRAM	7
5. SCHEMATIC DIAGRAM	9
6. PARTS LOCATION	23
7. IC DATA	27
8. EXPLODED VIEW AND PARTS LIST	33
9. ELECTRICAL PARTS LIST	37

Please use this service manual with referring to the user guide (D.F.U.) without fail.
修理の際は、必ず取扱説明書を準備し操作方法を確認の上作業を行ってください。

marantz®

CD7300

MARANTZ DESIGN AND SERVICE

Using superior design and selected high grade components, **MARANTZ** company has created the ultimate in stereo sound. Only original **MARANTZ** parts can insure that your **MARANTZ** product will continue to perform to the specifications for which it is famous.

Parts for your **MARANTZ** equipment are generally available to our National Marantz Subsidiary or Agent.

ORDERING PARTS :

Parts can be ordered either by mail or by Fax.. In both cases, the correct part number has to be specified.

The following information must be supplied to eliminate delays in processing your order :

1. Complete address
2. Complete part numbers and quantities required
3. Description of parts
4. Model number for which part is required
5. Way of shipment
6. Signature : any order form or Fax. must be signed, otherwise such part order will be considered as null and void.

USA

MARANTZ AMERICA, INC
1100 MAPLEWOOD DRIVE
ITASCA, IL. 60143
USA
PHONE : 630 - 741 - 0300
FAX : 630 - 741 - 0301

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MARANTZ EUROPE B.V.
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MARANTZ PROFESSIONAL PRODUCTS
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PROFESSIONAL AUSTRALIA

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BALMAIN, NSW 2041,
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CANADA

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633 GRANITE COURT,
PICKERING, ONTARIO L1W 3K1
CANADA
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FAX : 905 - 831 - 6936

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QualiFi Pty Ltd,
24 LIONEL ROAD,
MT. WAVERLEY VIC 3149
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THAILAND

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746 - 754 MAHACHAI ROAD.,
WANGBURAPAPIROM, PHRANAKORN,
BANGKOK, 10200 THAILAND
PHONE : +66 - 2 - 222 9181
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WO KEE HONG DISTRIBUTION PTE LTD
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NEW ZEALAND

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SUITE 8.1, LEVEL 8, MENARA GENESIS,
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50250 KUALA LUMPUR, MALAYSIA
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東京都渋谷区恵比寿南1-11-9

KOREA

MK ENTERPRISES LTD.
ROOM 604/605, ELECTRO-OFFICETEL, 16-58,
3GA, HANGANG-RO, YONGSAN-KU, SEOUL
KOREA
PHONE : +822 - 3232 - 155
FAX : +822 - 3232 - 154

SHOCK, FIRE HAZARD SERVICE TEST :

CAUTION : After servicing this appliance and prior to returning to customer, measure the resistance between either primary AC cord connector pins (with unit NOT connected to AC mains and its Power switch ON), and the face or Front Panel of product and controls and chassis bottom.

Any resistance measurement less than 1 Megohms should cause unit to be repaired or corrected before AC power is applied, and verified before it is return to the user/customer.

Ref. UL Standard No. 1492.

In case of difficulties, do not hesitate to contact the Technical
Department at above mentioned address.

1. TECHNICAL SPECIFICATIONS

Audio characteristics

Channels	2 channels
Frequency response	2 Hz to 20 kHz
Dynamic range	100 dB
Signal-to-noise ratio	110 dB
Channel separation	100 dB (1 kHz)
Harmonic distortion	0.0025% (1 kHz)
Wow & flutter	Precision of quartz
Error correction system	Cross-interleave Reed Solomon code (CIRC)
Audio output	2.0 V rms, stereo
Headphone output	18 mW/32 ohms (variable maximum)
Digital output	
Coaxial output (pin jack)	0.5 Vp-p, 75 ohms
Optical output (square optical connector)	-19 dBm

Optical readout system

Laser.....	AlGaAs semiconductor
Wavelength.....	780 nm

Signal system

Sampling frequency	44.1 kHz
Quantization	16-bit linear/channel

Power supply

Power requirement	AC 220V 60Hz (/C)
.....	AC 100V 50/60Hz (/F)
.....	AC 110/220V 50/60Hz (/K)
.....	AC 230V 50Hz (/N)
.....	AC 230V 50/60Hz (/S)

Power consumption	12 W
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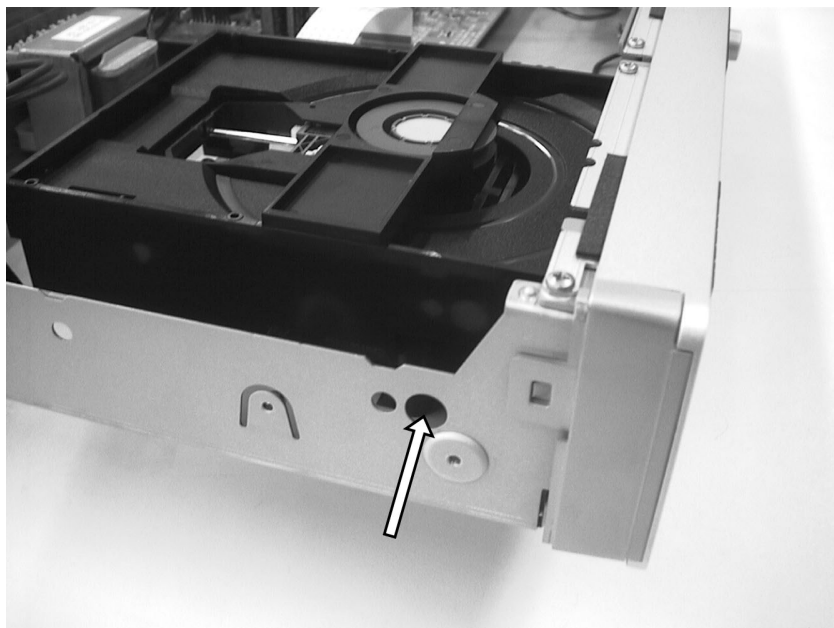
Cabinet, etc.

Maximum dimensions	440(W) x 89(H) x 317 (D) mm
Weight	5.7 kg
Allowable operating temperature	+5 to +35°C
Allowable operating humidity	5 to 90% (no condensation)

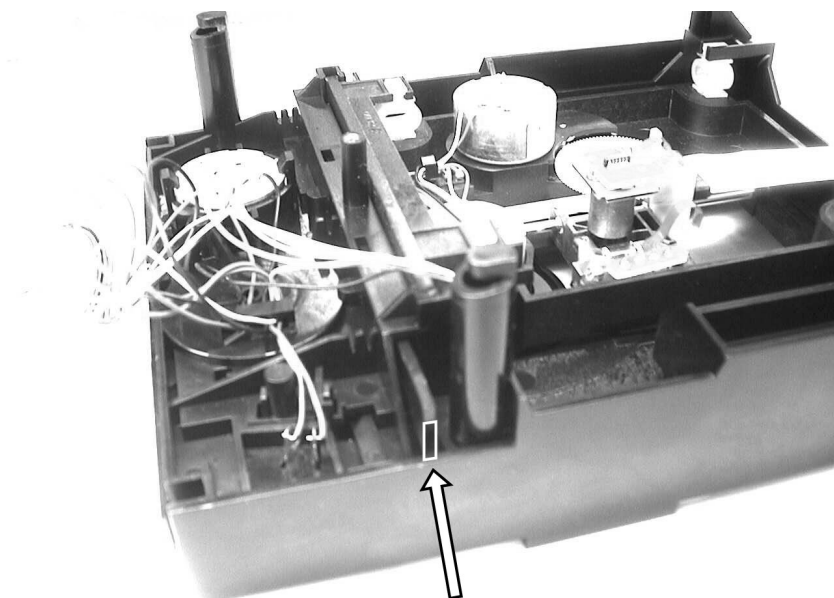
2. SERVICE PROCEDURE

Emergency Eject

1. To open the stuck tray, insert a pin into the eject pinhole and push the eject lever.
2. Use a pin $\phi 4\text{mm}$ or less.

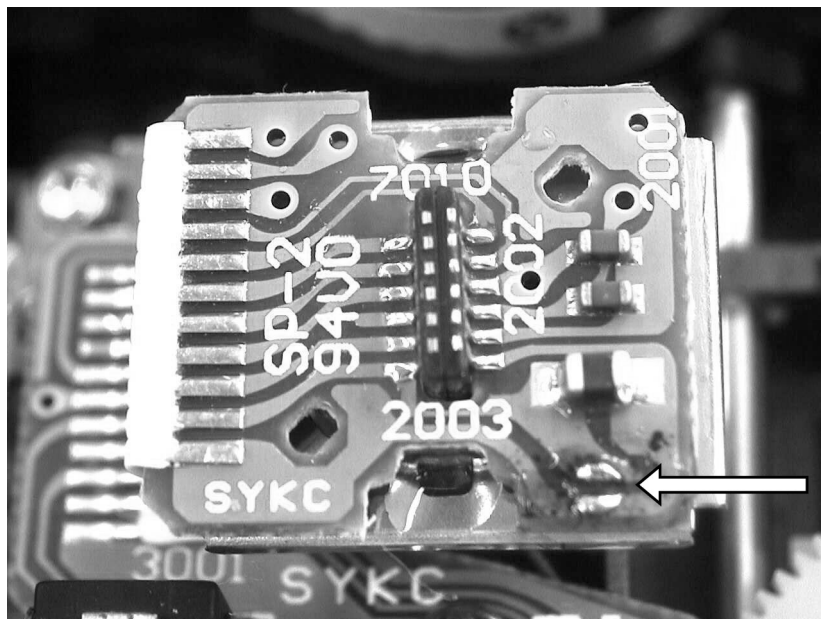


This picture shows the unit upside down. The eject lever is pointed by the arrow. The lever is thin so aim the narrow area carefully.



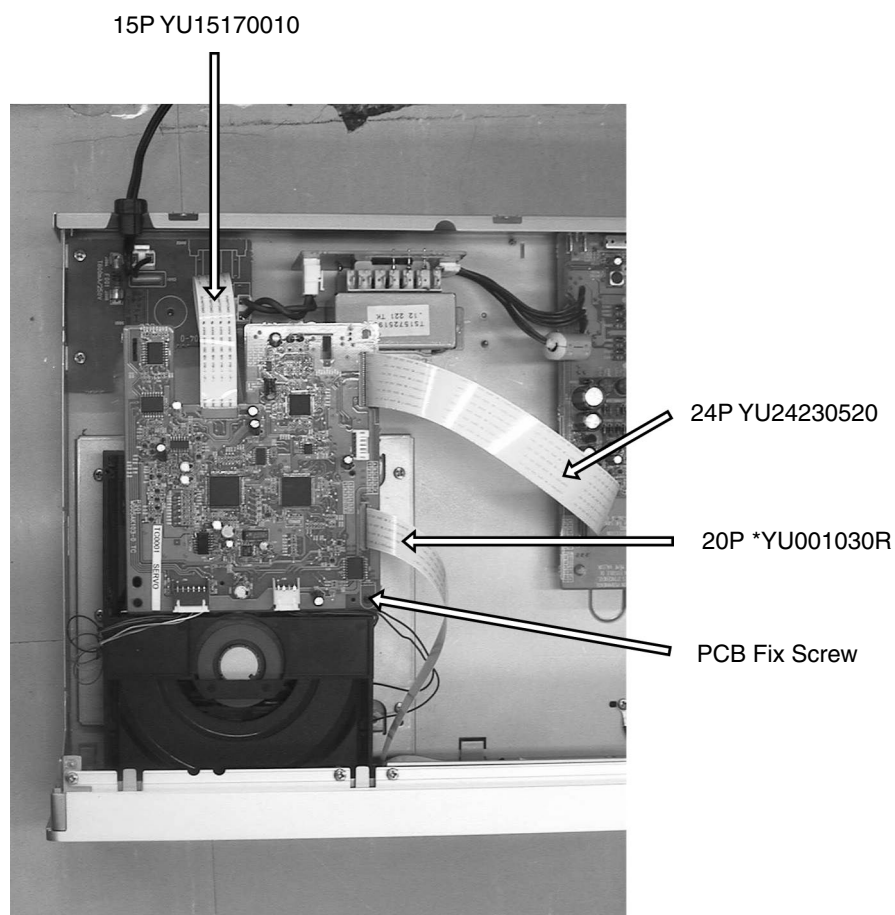
Cautions in Assembling and Disassembling

When removing the flat wire PS01, connecting the optical pick up and the CD decoder board, short-cut the two lands pointed by the arrow with solder. Otherwise the laser diode may be damaged by static electricity.



CD Decoder Board (PS01) Repair

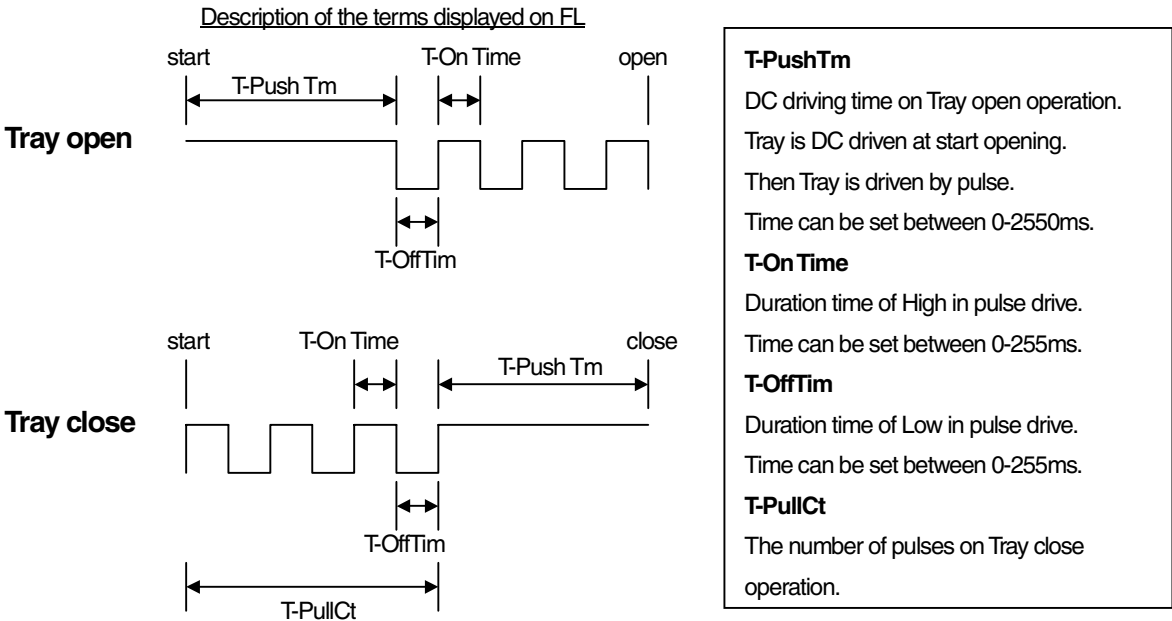
When Repairing the CD decoder board PS01, you can fix the board on the loader unit as shown in the picture below with the flat wires that are provided as service parts.



Disk Tray Open/Close Speed Adjustment

Open/Close speed of the tray can be changed.

When vibration occurs upon tray open/close and some unusual behavior happens, change the tray speed to stop vibration.



<Vibration on Disc Tray Open / Close>

Step 1. With pressing **DISPLAY** and **QUICK REPLAY** buttons, press **POWER** button.

Step 2. Press **EASY JOG** button.

“**T-OnTime 015**” (means Tray On Time 015ms default) is displayed.

Turn **EASY JOG** to change tray open/close speed from 000 to 255ms. Turn the number smaller to make the open/close speed slower. Turn the number bigger to make the speed faster. Set bigger number to stop the tray vibration. But the open/close speed becomes faster.

Press **EASY JOG** button again. The display changes as shown below so you can change other parameters for the open/close speed.

Display (Default Setting)	Description	Bigger Number	Smaller Number
T- OffTim 003	When open/close speed is too fast. Note) 001, 002, 003 cannot be set.	open/close speed becomes faster	open/close speed becomes slower
T- PushTm 070	When vibration cannot be stopped by adjusting T-OnTime .	open/close speed becomes slower	open/close speed becomes faster
T- PullCt 040	When disc clamp miss happens. Set smaller number to make it better.		

Step 3. Press **EASY JOG** button to complete adjustment.

Step 4. Press **STOP** button to store the settings in memory.

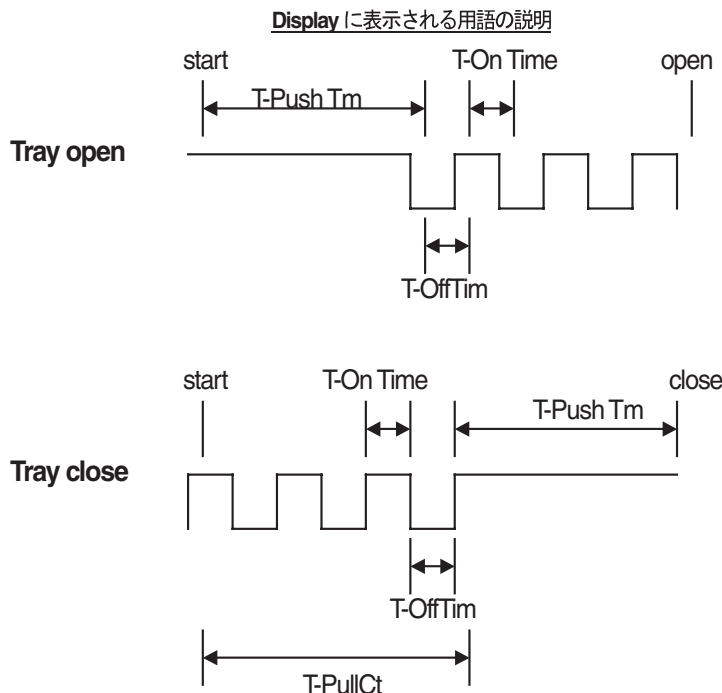
To reset all the settings to default status, press number **0** button in Service Mode 0

(Display : Ver *, ** P00). When miss-operated, press number **0** button to retry.

Disc Tray の Open/Close Speed 調整方法

この調整は、Disc Tray の open/close speed を可変できます。

特に、Tray open/close にて Tray が横揺れし、不自然な動作が発生した時に open/close の speed 調整にて、Tray の横揺れが無くなります。



T-PushTm

Tray open 動作時の DC 駆動時間です。

open の start は DC 駆動です。

その後パルス駆動になります。

0～2550ms 迄可変できます。

T-On Time

パルス駆動の High の時間です。

0～255ms 迄可変できます。

T-OffTim

パルス駆動の Low の時間です。

0～255ms 迄可変できます。

T-PullCt

Tray close 動作時のパルスの数です。

<Tray open/close 時の横揺れ>

手順 1. **DISPLAY** と **QUICK REPLAY** ボタンを押しながら **POWER** ボタンを押します。

手順 2. **EASY JOG** ボタンを PUSH ENTER します。

Display に “ **T-OnTime 015** ” (初期設定 Tray on time 015ms の意味) と表示。

EASY JOG ボタンを押し 000 から 255ms の範囲で Tray の open/close Speed が可変できます。

数字が小さくなると open/close Speed は遅くなります。数字が大きくなると早くなります。

Tray の open/close の横揺れは数字を大きくする事により、横揺れは、無くなります。

但し、open/close speed は遅くなります。

更に **EASY JOG** ボタンを PUSH ENTER すると、押す度に下記の表示に変わり

open/close speed を可変できます。

表示内容 (初期設定)	項 目	数字を小さくする	数字を大きくする
T- OffTim 003	open/close speed が速すぎる時 注) 000・001・002 は使用出来ません。	open/close speed が遅くなる	open/close speed が速くなる
T- PushTm 070	open 時の横揺れが T-OnTime で修正できない時 DC 駆動時間を増す	open/close speed が遅くなる	open/close speed が速くなる
T- PullCt 040	クランプミスの時、数字を小さくするとクランプミスが改善されます。		

手順 3. **EASY JOG** ボタンの PUSH ENTER で設定が決定します。

手順 4. **STOP** ボタンを押します。(メモリーされます)

初期設定に戻すときは、サービスモード O (表示は Ver: *, ** P00) の時、数字ボタン 0 のボタンを押します。

(操作を間違えた時も 0 のボタンを押し操作をやり直します)

3. SERVICE MODE

1. Turning into Service Mode

While pressing **DISPLAY** and **QUICK REPLAY** buttons, press **POWER** button.

2. Mode0 (Display : **Ver : *.*P00** Version number of the micro computer is displayed)

Status: [FOCUS OFF] [SPINDLE OFF] [RADIAL OFF] [MUTE ON]

- While pressing **▶▶** and **◀◀** buttons, the sledge moves toward the outer edge. Release the button makes the sledge return to the origin.

Press **EASY JOG** or **NEXT ▶▶** button on the remote to go to Mode 1.

3. Mode 1 (Display : **Ver : *.*P01**)

Status: [FOCUS ON] [SPINDLE OFF] [RADIAL OFF] [MUTE ON]

- Press **NEXT ▶▶** button to go to Mode 2.
- Press **PREV ◀◀** button to go to Mode 0.

4. Mode 2 (Display : **Ver : *.*P02**)

Status: [FOCUS ON] [SPINDLE ON] [RADIAL OFF] [MUTE ON]

- Press **NEXT ▶▶** button to go to Mode 3.
- Press **PREV ◀◀** button to go to Mode 1.

5. Mode 3 (Display : **Ver : *.*P03**)

Status: [FOCUS ON] [SPINDLE ON] [RADIAL ON] [MUTE OFF]

- Press **PREV ◀◀** button to go to Mode 1.

* In this Service Mode, all of the following button functions work in any status.

- 1) Press **DISPLAY** button to light up all the FL segments. Press it again then each FL segment lights up one by one.
- 2) Press **STOP** button. Then press a button on the unit. The name of the button is displayed. Pressing a button on the remote displays the RC-5 code of the button.
- 3) Press **PLAY** button in Mode 1, 2 or 3 then normal operation can be performed. If an error occurs the error number is displayed. (Ex : Err 10) See the table below.

6. Terminating Service Mode

Turn off power to quit Service Mode.

3. サービスモード

1. サービスモードへの入り方

DISPLAYと**QUICK REPLAY**ボタンを押しながら**POWER**ボタンを押します。

2. モード0 (表示 Ver : *.* **P00** マイコンの Ver. が表示されます)

状態 : [FOCUS OFF] [SPINDLE OFF] [RADIAL OFF] [MUTE ON]

- **▶▶**、**◀◀** ボタンを押している間だけスレッドが外周へ移動します。ボタンを放すと原点に戻ります。

EASY JOGまたはリモコンの **NEXT ▶▶** ボタンを押すとモード1へ移行します。

3. モード1 (表示 Ver : *.* **P01**)

状態 : [FOCUS ON] [SPINDLE OFF] [RADIAL OFF] [MUTE ON]

- **NEXT ▶▶** ボタンを押すとモード2へ移行します。
- **PREV ◀◀** ボタンを押すとモード0へ移行します。

4. モード2 (表示 Ver : *.* **P02**)

状態 : [FOCUS ON] [SPINDLE ON] [RADIAL OFF] [MUTE ON]

- **NEXT ▶▶** ボタンを押すとモード3へ移行します。
- **PREV ◀◀** ボタンを押すとモード1へ移行します。

5. モード3 (表示 Ver : *.* **P03**)

状態 : [FOCUS ON] [SPINDLE ON] [RADIAL ON] [MUTE OFF]

- **PREV ◀◀** ボタンを押すとモード2へ移行します。

* サービスモードの全ての状態で以下のボタンが有効です。

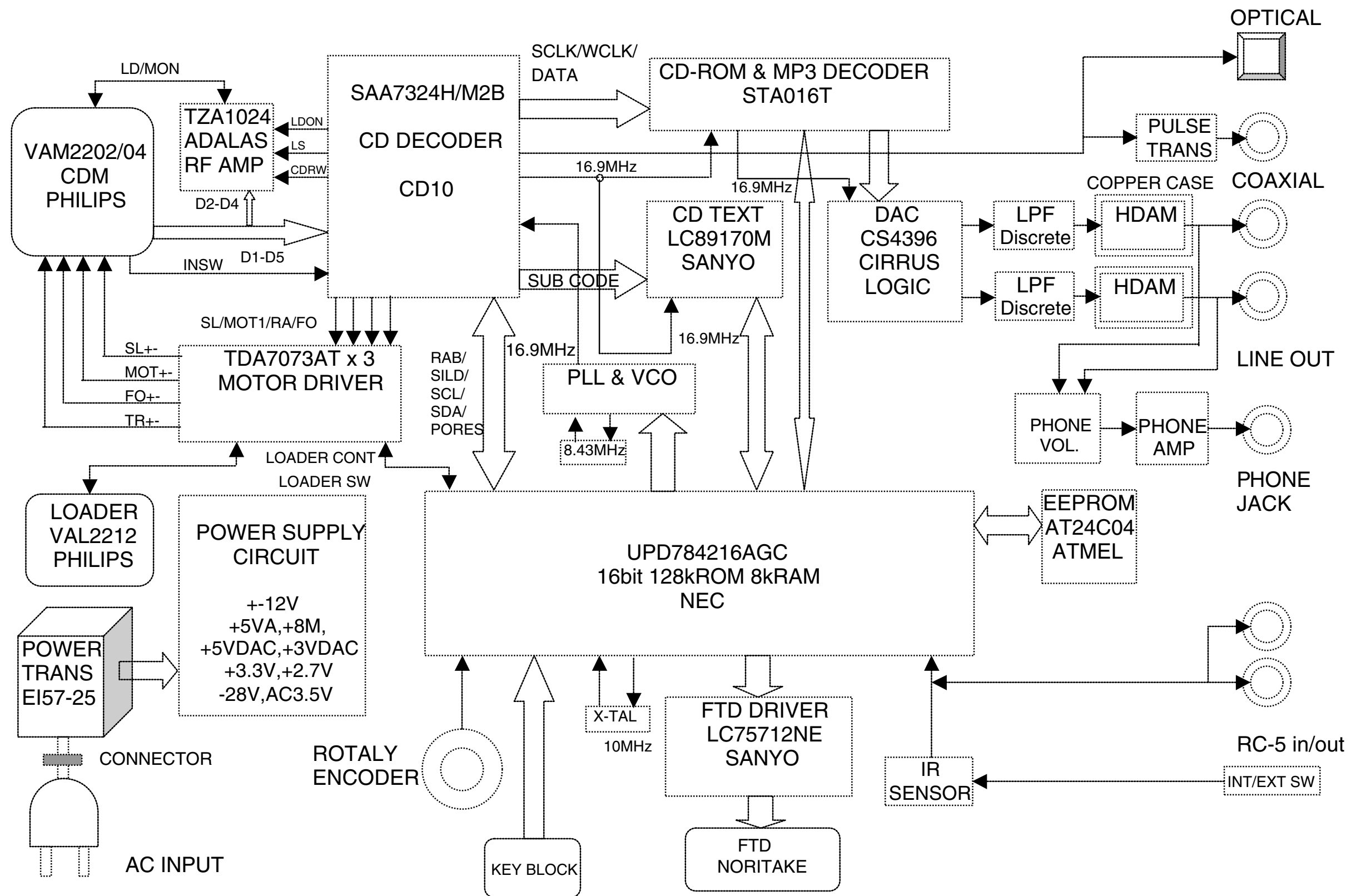
- 1) **DISPLAY** ボタンを押すと FL が全点灯します。もう一度押すと各セグメントが順次点灯します。
- 2) **STOP** ボタンを押し、更に本体のボタンを押すとボタンの名前が表示されます。リモコンのボタンを押すと RC-5 コードが表示されます。
- 3) モード1. 2. 3の状態では **PLAY** ボタンを押すと通常と同じ動作となります。ただし、動作中、異常が確認された時にエラー番号が表示されます。(例 : Err 10) 下記の表を参考にしてください。

6. サービスモードの解除

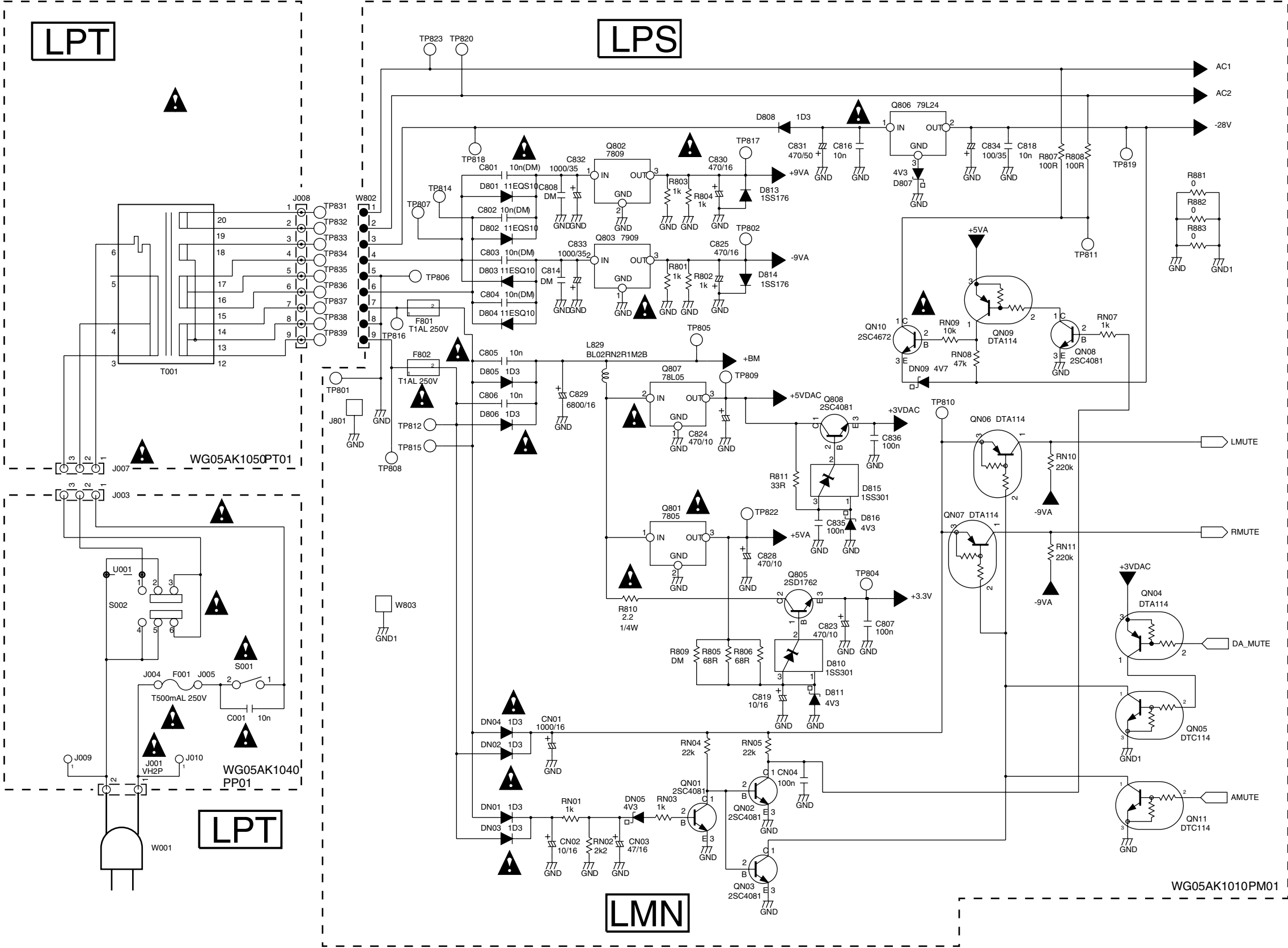
電源を切るとサービスモードが解除されます。

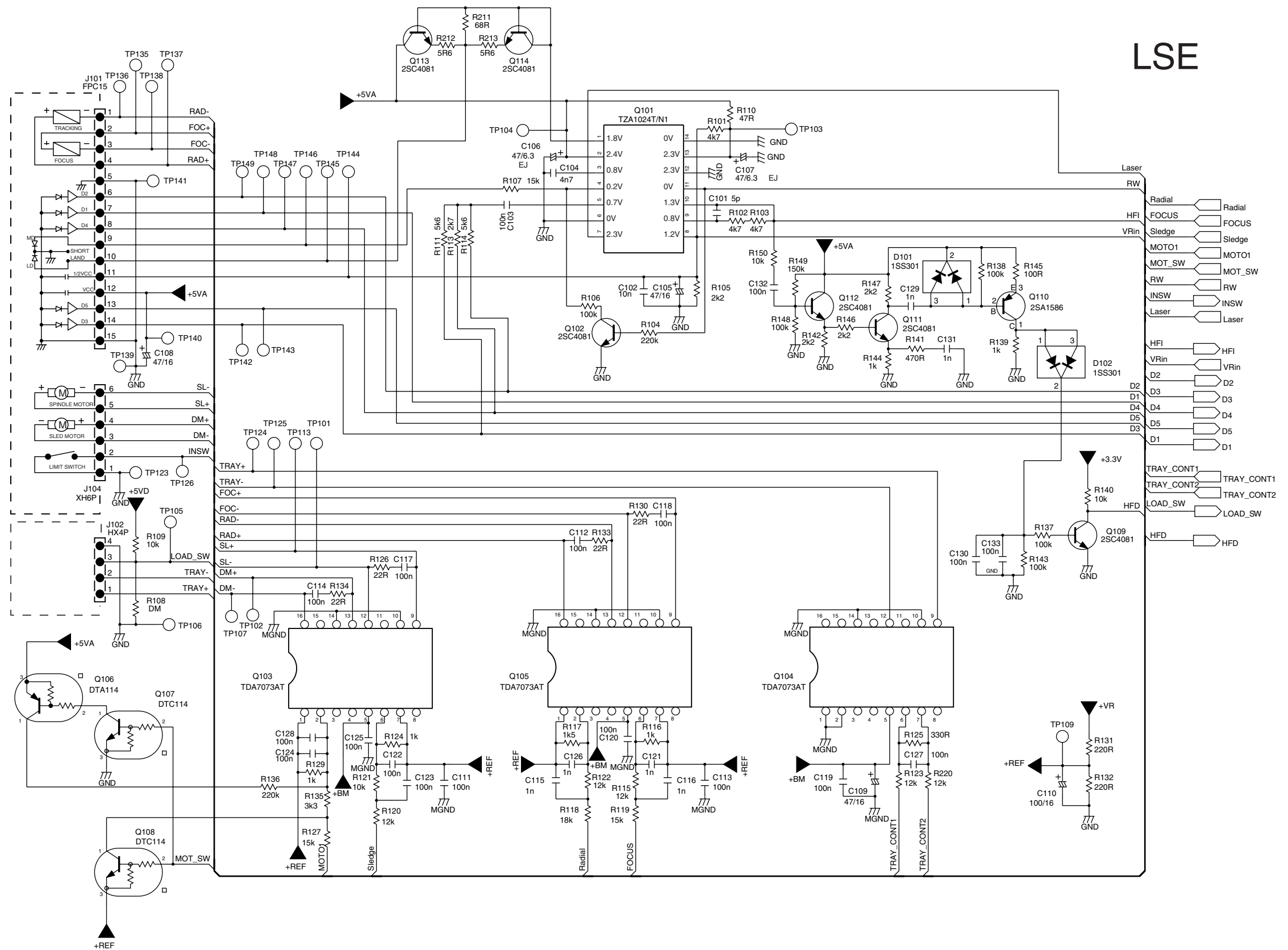
Error Code	Error
Err 02	FOCUS Error
Err 07	SUB CODE Error
Err 08	T. O. C. Error
Err 09	DECODER Error
Err 10	RADIAL Error
Err 11, 12	SLEDGE Error
Err 13	SPINDLE Error
Err 16 ~ 20	SEARCH Error
Err 30	DOOR Error
Err 31	TRAY Error
Err 32 ~ 47	BUTTON INPUT Error

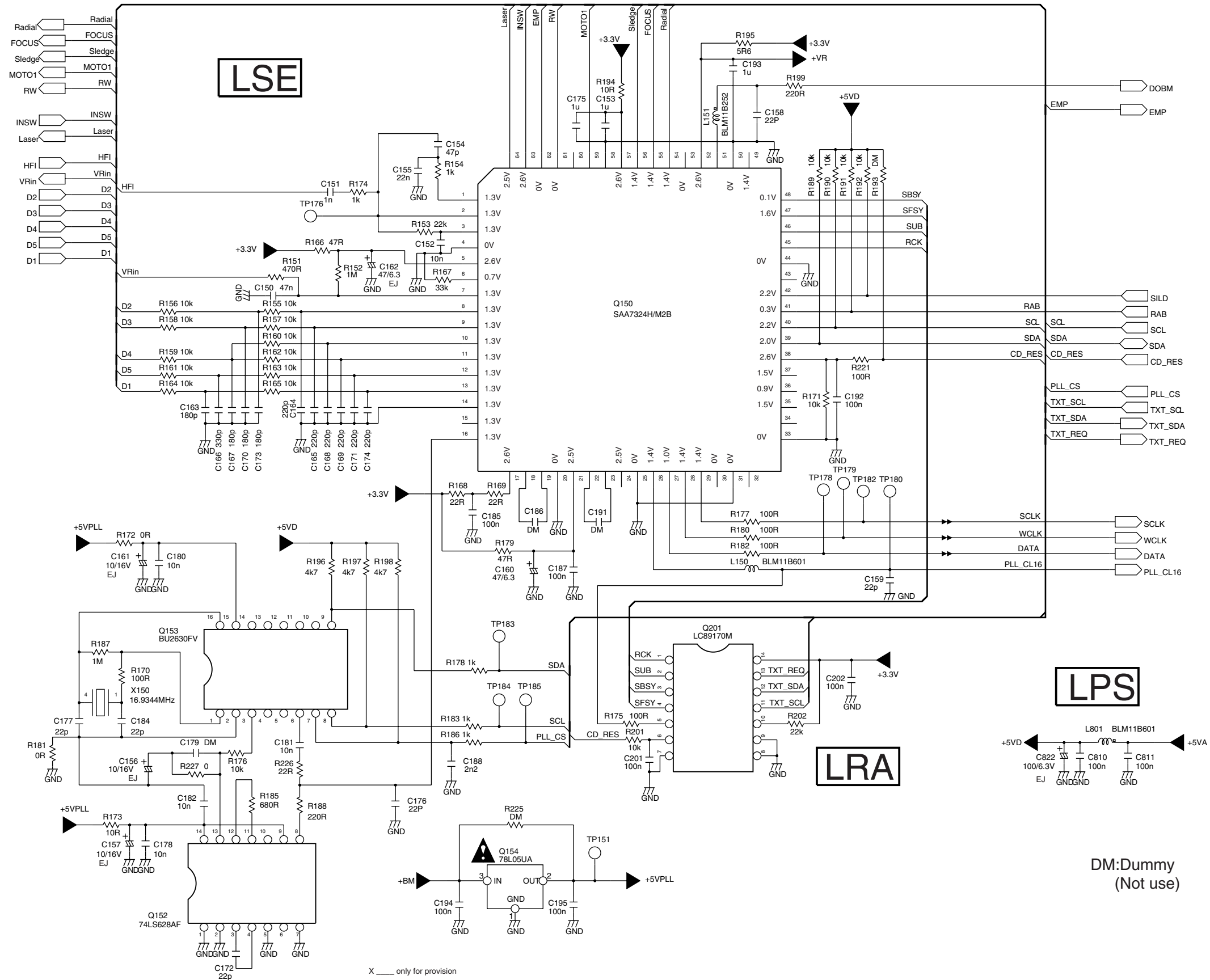
4. BLOCK DIAGRAM

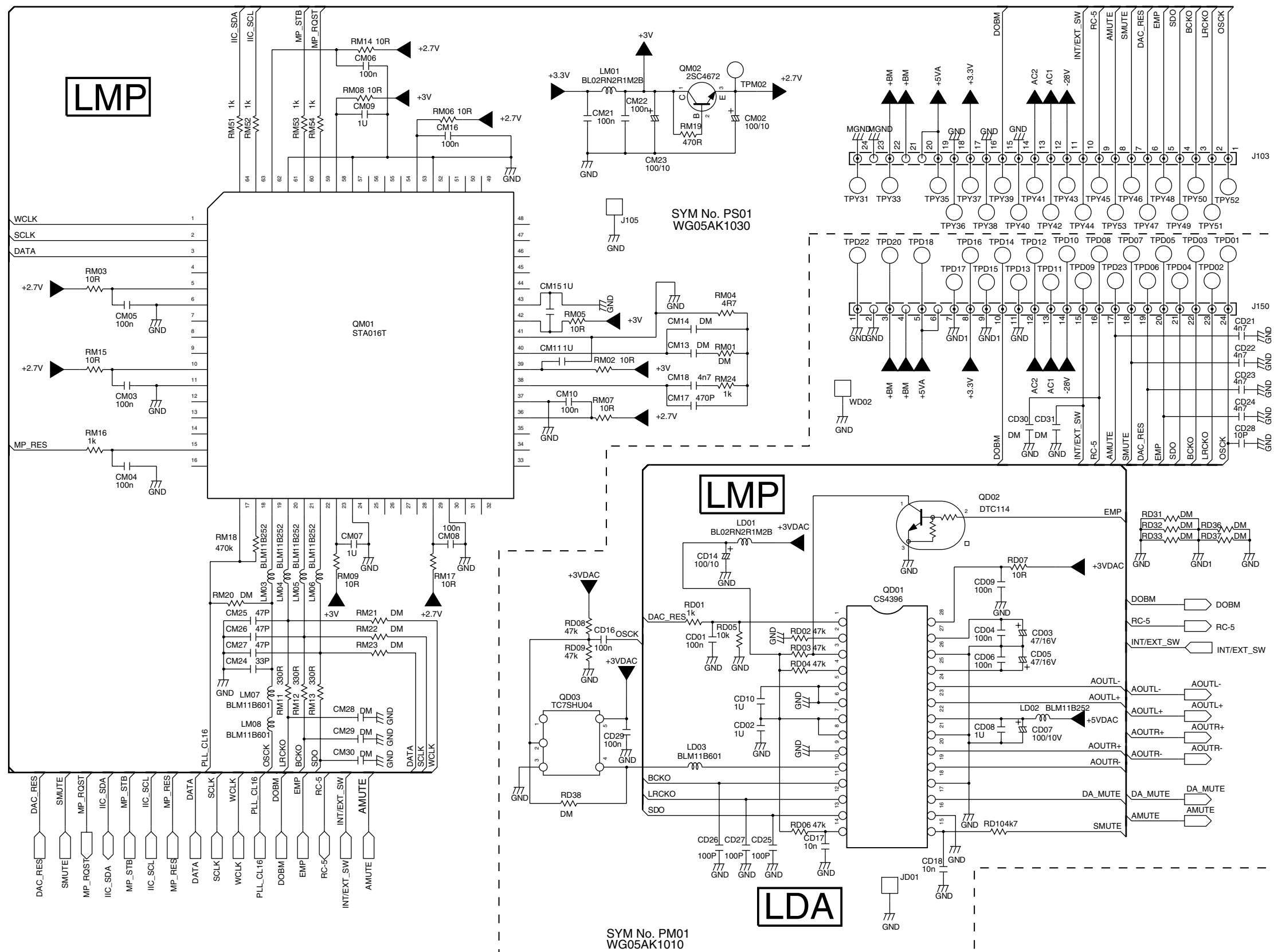


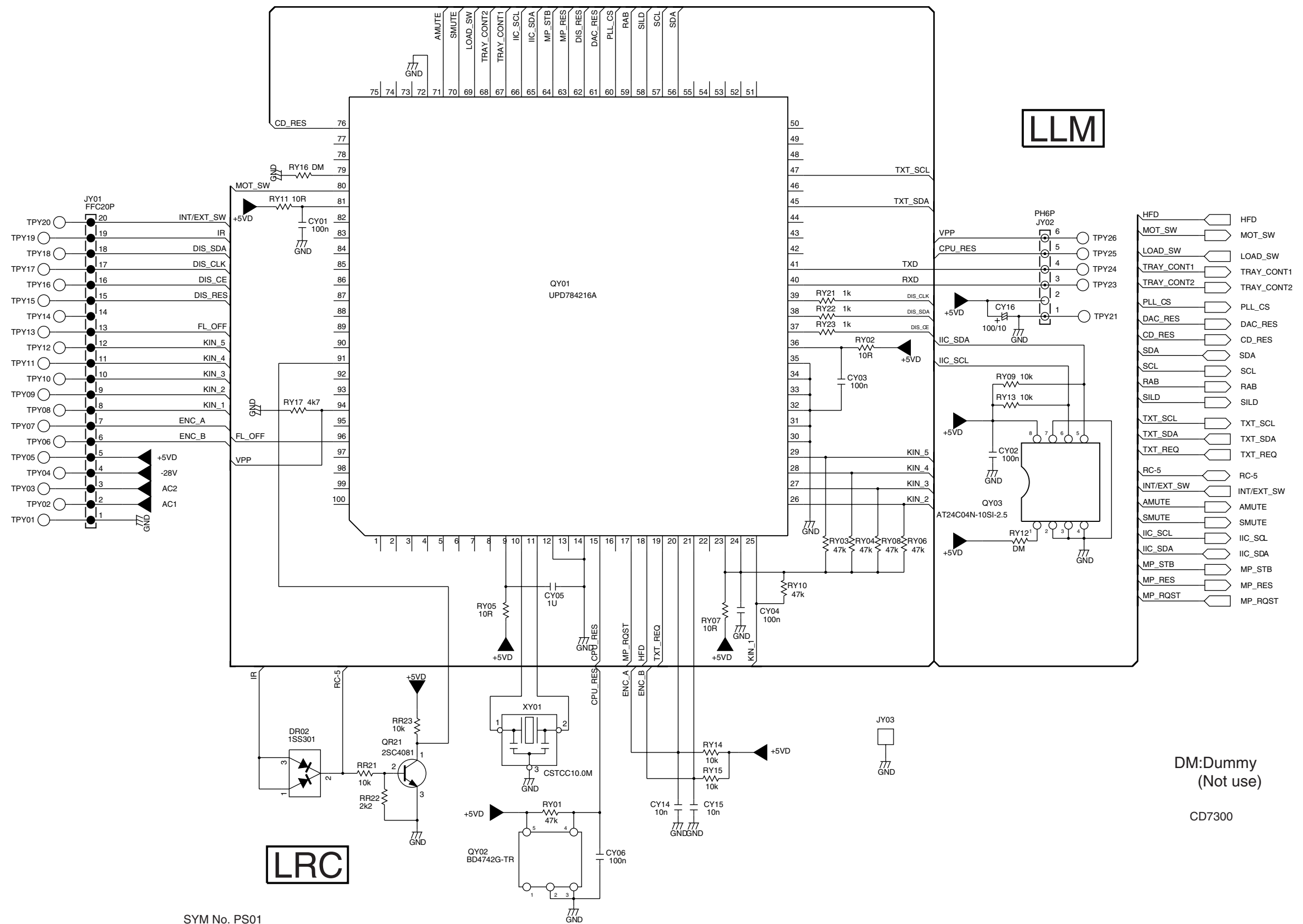
5. SCHEMATIC DIAGRAM

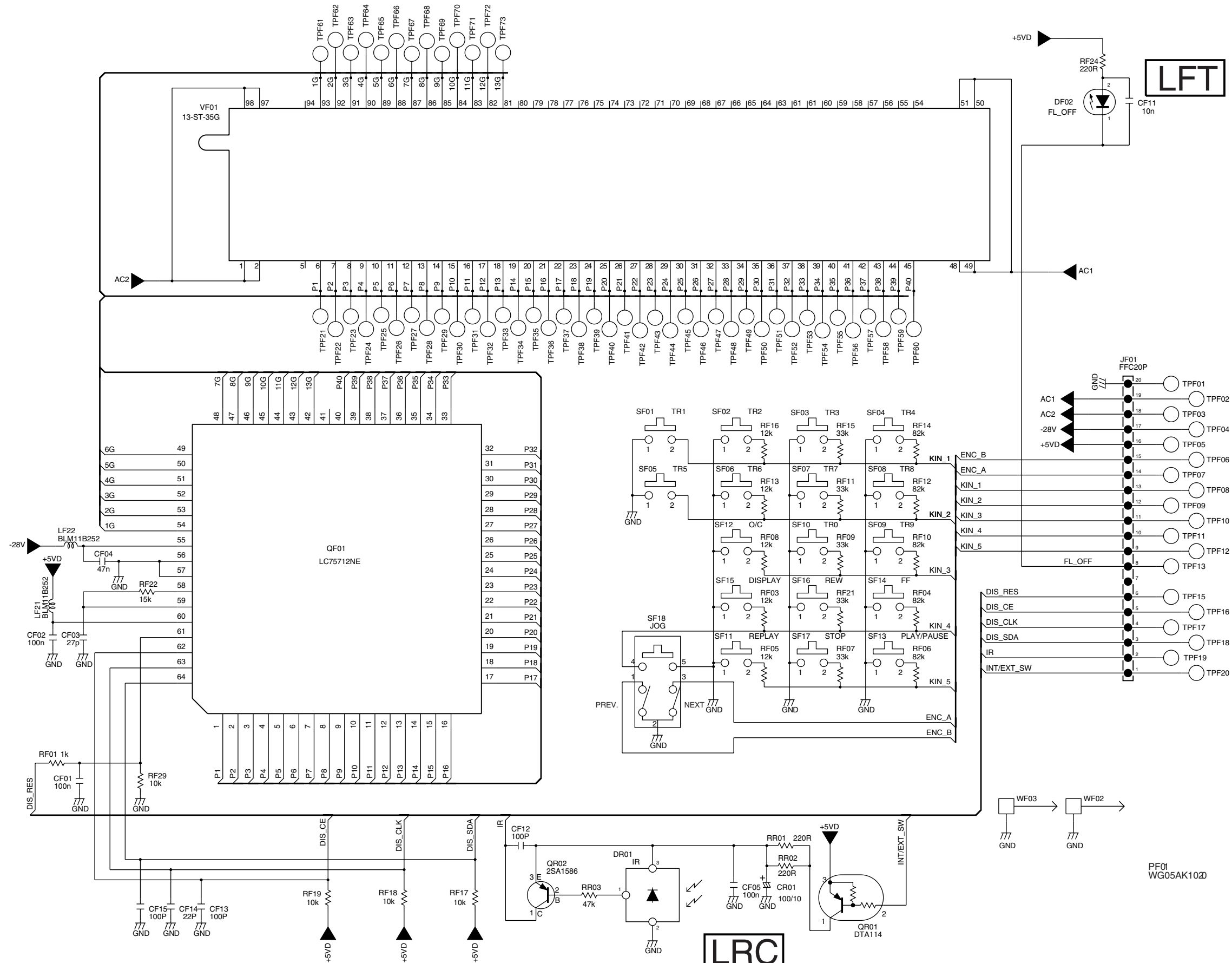






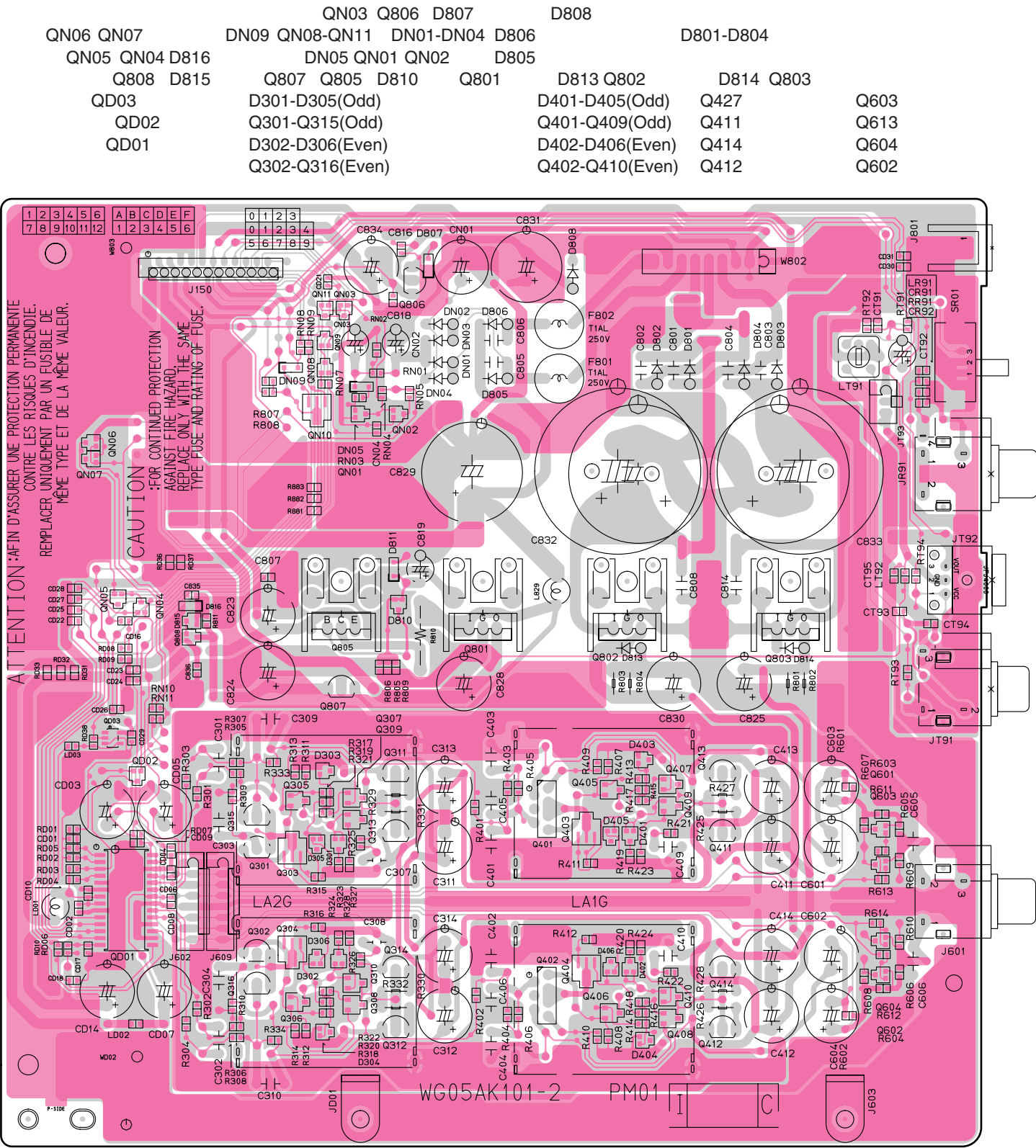




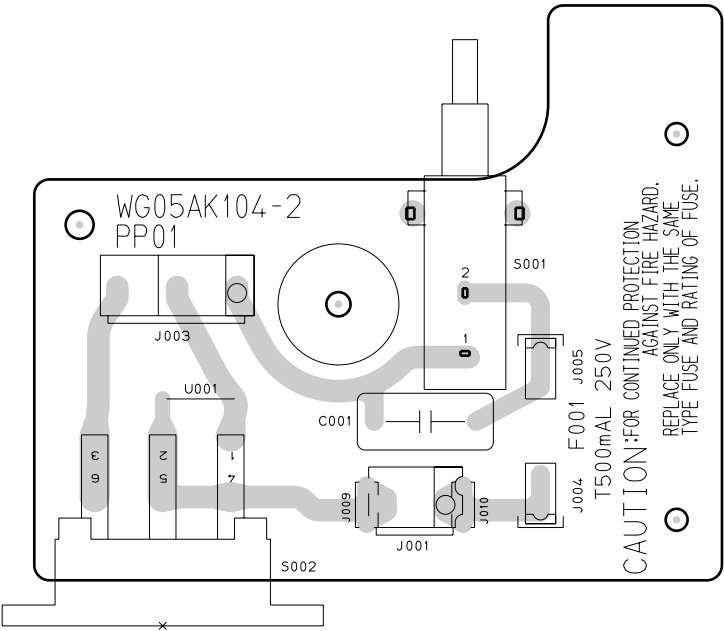


6. PARTS LOCATION

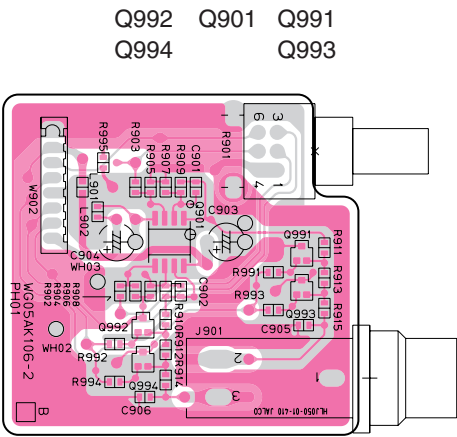
PM01



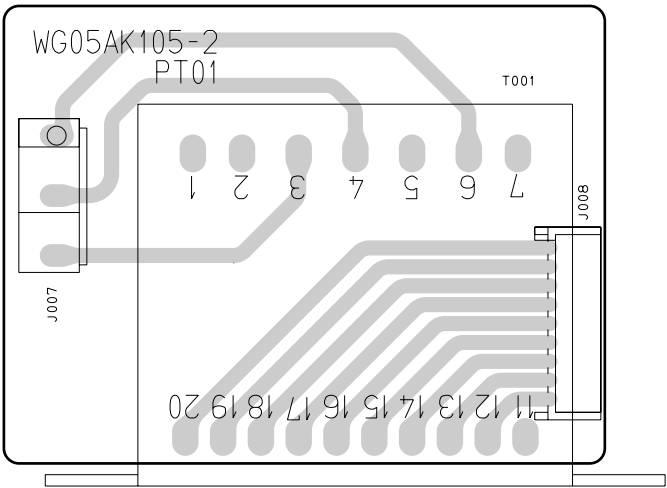
PP01



PH01



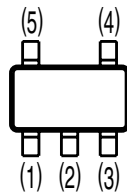
PT01



7. IC DATA

QY02:BD4742G

Pin Assignment



1pin : N.C.
2pin : SUB
3pin : GND
4pin : Vout
5pin : Vcc

Q153:BU2630FV

Pin descriptions

Pin No.	Pin name	Name	Function	I/O circuit
16	XOUT	Crystal resonator	For reference frequency	TYPE A
1	XIN			
2	V _{SS}			
3	RPD	Phase comparator output	This is LO if the locally divided value is higher than the reference frequency, HI if it is lower, and Z if it matches.	TYPE E
4	P—R	Output port	This is controlled by the input data.	TYPE D
5	RON			
6	F—R	VCO input	Local input for reception	TYPE F
7	CE	Chip enable clock signal serial data	When CE is HIGH, the DA synchronized to the rise of CK is read into the internal shift register, and is latched at the timing of the CE fall.	TYPE B
8	CK			
9	DA			
10	LD	Unlock output	This goes ON when the PLL is unlocked on the transmission side	TYPE D
11	F—T	VCO input	Local input for transmission	TYPE F
12	TON	Output port	This is controlled by the input data	TYPE D
13	P—T			
14	TPD	Phase comparator output	This is LO if the locally divided value is higher than the reference frequency, HI if it is lower, and Z if it matches.	TYPE E
15	V _{DD}	Power supply	2.5~5.5V	

QD01:CS4396

Pin Description

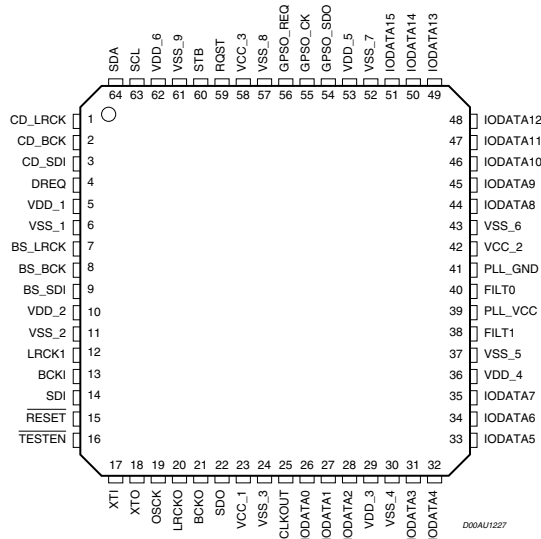
Reset	RST	1	28	VREF	Voltage Reference
See Description	M4(AD0/CS)	2	27	FILT+	Reference Filter
See Description	M3(AD1/CDIN)	3	26	FILT-	Reference Ground
See Description	M2(SCL/CCLK)	4	25	CMOUT	Common ModeS Voltage
See Description	M0(SDA/CDOUT)	5	24	AOUTL-	Differential Output
Digital Ground	DGND	6	23	AOUTL+	Differential Output
Digital Power	VD	7	22	VA	Analog Power
Digital Power	VD	8	21	AGND	Analog Ground
Digital Ground	DGND	9	20	AOUTR+	Differential Output
Master Clock	MCLK	10	19	AOUTR-	Differential Output
Serial Clock	SCLK	11	18	AGND	Analog Ground
Left/Right Clock	LRCK	12	17	MUTEC	Mute Control
Serial Data	SDATA	13	16	C/H	Control port/Hardware select
See Description	M1	14	15	MUTE	Soft Mute

Pin Description

PIN	Pin Name	Type	Description	Sourde/Dest
CDDSP interface				
1	CD_LRCK	I	DSP Interface left/right Clock	From DSP
3	CD_SDI	I	DSP interface serial data	From DSP
2	CD_BCK	I	DSP interface bit clock	From DSP
SDI interface				
9	BS_SDI	I	Bitstream interface serial data	From MCU
7	BS_LRCK	I	Bitstream interface left/right Clock	From MCU
8	BS_BCK	I	Bitstream interface clock	From MCU
4	DREQ	O	Bitstream data request	To MCU
PCM IN interface				
13	BCKI	I	ADC bit clock	From ADC
14	SDI	I	ADC serial data	From ADC
12	LRCKI	I	ADC left/right Clock	From ADC
PCM OUT interface				
20	LRCKO	O	DAC Interface left/right Clock	To DAC
22	SDO	O	DAC serial data	To DAC
21	BCKO	O	DAC bit clock	To DAC
19	OSCK	O	DAC oversampling clock	To DAC/ADC
GPSO interface				
55	GPSO_CK	I	GPSO bit clock	From MCU
54	GPSO_SDO	O	GPSO serial data	To MCU
56	GPSO_REQ	O	GPSO request signal	To MCU
GPIO interface				
26	IODATA0	I/O	GPIODATA0	
27	IODATA1	I/O	GPIODATA1	
28	IODATA2	I/O	GPIODATA2	
31	IODATA3	I/O	GPIODATA3	
32	IODATA4	I/O	GPIODATA4	
33	IODATA5	I/O	GPIODATA5	
34	IODATA6	I/O	GPIODATA6	
35	IODATA7	I/O	GPIODATA7	
44	IODATA8	I/O	GPIODATA8	
45	IODATA9	I/O	GPIODATA9	
46	IODATA10	I/O	GPIODATA10	
47	IODATA11	I/O	GPIODATA11	
48	IODATA12	I/O	GPIODATA12	
49	IODATA13	I/O	GPIODATA13	
50	IODATA14	I/O	GPIODATA14	
51	IODATA15	I/O	GPIODATA15	

PIN	Pin Name	Type	Description	Sourde/Dest
HANDSHAKE SIGNALS				
60	STB	I	Strobe signal	From MCU
59	RQST	O	I2C data signal	To MCU
I²C LINK				
63	SCL	I	I2C clock signal	From MCU
64	SDA	I/O	I2C data signal	To MCU
MISCELLANEOUS				
17	XTI	I	Oscillator input	
18	XTO	O	Oscillator output	
25	CLKOUT	O	Buffered output clock	
15	-RESET	I	Reset	
16	-TESTEN	I	Reserved for test purpose	
40	FILT0	I	PLL external filter	
38	FILT1		PLL external filter	
POWER SUPPLY				
39	PLL_VCC			
41	PLL_GND			
5	VDD_1		Digital supply (2.5V Power Supply)	
10	VDD_2		Digital supply (2.5V Power Supply)	
29	VDD_3		Digital supply (2.5V Power Supply)	
36	VDD_4		Digital supply (2.5V Power Supply)	
53	VDD_5		Digital supply (2.5V Power Supply)	
62	VDD_6		Digital supply (2.5V Power Supply)	
23	VCC_1		Digital supply (3.3V Power Supply)	
42	VCC_2		Digital supply (3.3V Power Supply)	
58	VCC_3		Digital supply (3.3V Power Supply)	
6	VSS_1			
11	VSS_2			
24	VSS_3			
30	VSS_4			
37	VSS_5			
43	VSS_6			
52	VSS_7			
57	VSS_8			
61	VSS_9			

Pin Assignment



Pin Description

SYMBOL	PIN	DESCRIPTION
HFREF	1	comparator common mode input
HFIN	2	comparator signal input
ISLICE	3	current feedback output from data slicer
V _{SSA1}	4 ⁽¹⁾	analog ground 1
V _{DDA1}	5 ⁽¹⁾	analog supply voltage 1
I _{ref}	6	reference current output pin
V _{RIN}	7	reference voltage for servo ADC's
D1	8	unipolar current input (central diode signal input)
D2	9	unipolar current input (central diode signal input)
D3	10	unipolar current input (central diode signal input)
D4	11	unipolar current input (central diode signal input)
R1	12	unipolar current input (satellite diode signal input)
R2	13	unipolar current input (satellite diode signal input)
V _{SSA2}	14 ⁽¹⁾	analog ground 2
CROUT	15	crystal/resonator output
CRIN	16	crystal/resonator input
V _{DDA2}	17 ⁽¹⁾	analog supply voltage 2
LN	18	DAC left channel differential output - negative
LP	19	DAC left channel differential output - positive
V _{neg}	20 ⁽¹⁾	DAC negative reference supply (equivalent to DAC V _{SS})
V _{pos}	21 ⁽¹⁾	DAC positive reference supply (equivalent to DAC V _{DD})
RN	22	DAC right channel differential output - negative
RP	23	DAC right channel differential output - positive
SELPLL	24	selects whether internal clock multiplier PLL is used
TEST1	25	test control input 1; this pin should be tied LOW
CL16	26	16.9344 MHz system clock output
DATA	27	serial data output (3-state)
WCLK	28	word clock output (3-state)
SCLK	29	serial bit clock output (3-state)
EF	30	C2 error flag output (3-state)
TEST2	31	test control input 2; this pin should be tied LOW
KILL	32	kill output (programmable; open-drain)
V _{SSD1}	33 ⁽¹⁾	digital ground 2
V2/V3	34	versatile I/O: input versatile pin 2 or output versatile pin 3 (open-drain)
WCLI	35	word clock input (for data loopback to DAC)
SDI	36	serial data input (for data loopback to DAC)
SCLI	37	serial bit clock input (for data loopback to DAC)
RESET	38	power-on reset input (active LOW)
SDA	39	microcontroller interface data I/O line (open-drain output)
SCL	40	microcontroller interface clock line input
RAB	41	microcontroller interface R $\overline{W}$ and load control line input (4-wire bus mode)
SILD	42	microcontroller interface $\overline{R/W}$ and load control line input (4-wire bus mode)
STATUS	43	servo interrupt request line/decoder status register output (open-drain)
TEST3	44	test control input 3; this pin should be tied LOW
RCK	45	subcode clock input
SUB	46	P-to-W subcode bits output (3-state)
SFSY	47	subcode frame sync output (3-state)
SBSY	48	subcode block sync output (3-state)
CL11/4	49	11.2896 MHz or 4.2336 MHz (for microcontroller) clock output
V _{SSD2}	50 ⁽¹⁾	digital ground 3
DOBM	51	bi-phase mark output (externally buffered; 3-state)
V _{DD1(P)}	52 ⁽¹⁾	digital supply voltage 2 for periphery
CFLG	53	correction flag output (open-drain)
RA	54	radial actuator output
FO	55	focus actuator output
SL	56	sledge control output
V _{DD2(C)}	57 ⁽¹⁾	digital supply voltage 3 for core
V _{SSD3}	58 ⁽¹⁾	digital ground 4
MOTO1	59	motor output 1; versatile (3-state)
MOTO2	60	motor output 2; versatile (3-state)
V4	61	versatile output pin 4
V5	62	versatile output pin 5
V1	63	versatile input pin 1
LDON	64	laser drive on output (open-drain)

Note

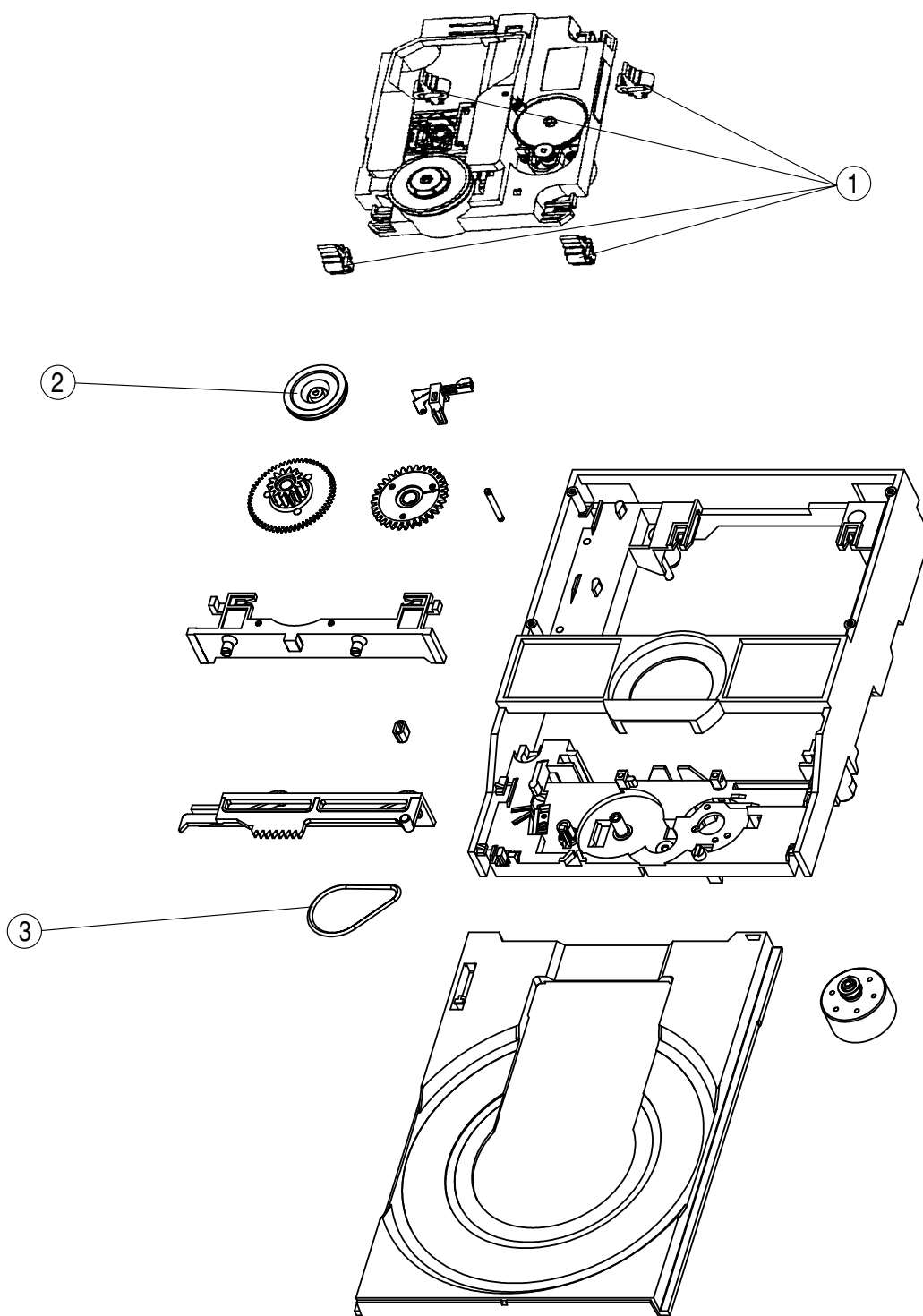
1. All supply pins must be connected to the same external power supply voltage

Pin No.	Port Name	I/O	Signal Name	Pull up	Initial	Remarks
1	P120/RTP0	O	n.c.		L	n.c.
2	P121/RTP1	O	n.c.		L	n.c.
3	P122/RTP2	O	n.c.		L	n.c.
4	P123/RTP3	O	n.c.		L	n.c.
5	P124/RTP4	O	n.c.		L	n.c.
6	P125/RTP5	O	n.c.		L	n.c.
7	P126/RTP6	O	n.c.		L	n.c.
8	P127/RTP7	O	n.c.		L	n.c.
9	VDD		+3.3V			Power +3.3V
10	X2	I	5MHz			Main system clock 5MHz
11	X1	I	5MHz			Main system clock 5MHz
12	VSS		GND			GND
13	XT2		n.c.			n.c.
14	XT1		GND			GND
15	/RESET	I	CPU_RES			Reset active low
16	P00/INTP0	I	IR	int.		RC-5 signal in
17	P01/INTP1	I	MP_RQST	int.		from STA016T
18	P02/INTP2/NMI	I	HFD	int.		from HF Detector
19	P03/INTP3	I	TXT_REQ	int.		from LC89170M
20	P04/INTP4	I	ENC_A	int.		Rotary Encoder A signal(C.W)
21	P05/INTP5	I	ENC_B	int.		Rotary Encoder B signal(C.C.W)
22	P06/INTP6	I	n.c.	int.		
23	AVDD		+3.3V			Power +3.3V
24	AVREF0		+3.3V			A/D reference
25	P10/ANI0	I	KIN_1	ext.		A/D 4-key input
26	P11/ANI1	I	KIN_2	ext.		A/D 4-key input
27	P12/ANI2	I	KIN_3	ext.		A/D 4-key input
28	P13/ANI3	I	KIN_4	ext.		A/D 4-key input
29	P14/ANI4	I	KIN_5	ext.		A/D 4-key input
30	P15/ANI5	I	GND			GND
31	P16/ANI6	I	GND			GND
32	P17/ANI7	I	GND			GND
33	AVSS		GND			GND
34	P130/ANO0	I	GND			GND
35	P131/ANO1	I	GND			GND
36	AVREF1		+3.3V			D/A reference
37	P70/RXD2/SI2	O	DIS_CE	int.	L	Latch for LC7571x
38	P71/TXD2/SO2	O	DIS_SDA	int.	L	Data for LC7571x
39	P72/ASCK2/SCK2	O	DIS_SCL	int.	L	Clock for LC7571x
40	P20/RXD1/SI1	I	RXD			for PcLink
41	P21/TXD1/SO1	O	TXD			for PcLink
42	P22/ASCK1/SCK1	O	n.c.		L	
43	P23/PCL	O	n.c.		L	
44	P24/BUZ	O	n.c.		L	
45	P25/SI0/SDA0	I	TXT_SDA	int.		Data for LC89170M
46	P26/SO0	O	n.c.		L	
47	P27/SCK0/SCL0	O	TXT_SCK	int.	L	Clock for LC89170M
48	P80/A0	O	n.c.		L	n.c.
49	P81/A1	O	n.c.		L	n.c.
50	P82/A2	O	n.c.		L	n.c.
51	P83/A3	O	n.c.		L	n.c.
52	P84/A4	O	n.c.		L	n.c.
53	P85/A5	O	n.c.		L	n.c.

Pin No.	Port Name	I/O	Signal Name	Pull up	Initial	Remarks
54	P86/A6	O	n.c.		L	n.c.
55	P87/A7	O	n.c.		L	n.c.
56	P40/AD0	I/O	CD_SDA	ext.	H	Data for CD10/MB15U10
57	P41/AD1	O	CD_SCL	ext.	H	Clock for CD10/MB15U10
58	P42/AD2	O	CD_SILD	int.	H	Latch for CD10 servo part
59	P43/AD3	O	CD_RAB	int.	L	Latch for CD10 decoder part
60	P44/AD4	O	PLL_CS	int.	L	CS for MB15U10
61	P45/AD5	O	CD_RES	int.	L	Reset for CD10/LC89170M
62	P46/AD6	O	DIS_RES	int.	L	Reset for LC7571x
63	P47/AD7	O	MP_RES	int.	L	Reset for STA016T
64	P50/A8	O	MP_STB	int.	L	Strobe for STA016T
65	P51/A9	I/O	IIC_SDA	ext.	L	Data for STA016T/AT24C4
66	P52/A10	O	IIC_SCL	ext.	L	Clock for STA016T/AT24C4
67	P53/A11	O	TRAY_CONT	ext.	HighZ	Tray in/out control
68	P54/A12	I	LOAD_SW	ext.		Tray loading sw Low active
69	P55/A13	O	SMUTE	int.	L	Soft mute for DAC
70	P56/A14	O	n.c.		L	n.c.
71	P57/A15	O	n.c.		L	n.c.
72	VSS		GND			GND
73	P60/A16	O	n.c.		L	n.c.
74	P61/A17	O	n.c.		L	n.c.
75	P62/A18	O	n.c.		L	n.c.
76	P63/A19	O	n.c.		L	n.c.
77	P64/RD	O	n.c.		L	n.c.
78	P65/WR	O	n.c.		L	n.c.
79	P66/WAIT	I	MP3_EN	int.	L	MP3 Enable = Open MP3 Disable = LOW
80	P67/ASTB	O	MOT_SW	int.	L	Spindle Free RUN Active HIGH
81	VDD		+3.3V			Power +3.3V
82	P100/TI5/TO5	O	n.c.		L	n.c.
83	P101/TI6/TO6	O	n.c.		L	n.c.
84	P102/TI7/TO7	O	n.c.		L	n.c.
85	P103/TI8/TO8	O	n.c.		L	n.c.
86	P30/TO0	O	n.c.		L	n.c.
87	P31/TO1	O	n.c.		L	n.c.
88	P32/TO2	O	n.c.		L	n.c.
89	P33/TI1	O	n.c.		L	n.c.
90	P34/TI2	O	n.c.		L	n.c.
91	P35/TI00	O	n.c.		L	n.c.
92	P36/TI01	O	n.c.		L	n.c.
93	P37/EXA	O	n.c.		L	n.c.
94	TEST		GND			GND
95	P90	O	S_PLAY	ext.	H	LED L active
96	P91	O	FL_OFF	ext.	H	LED L active
97	P92	O	n.c.		L	n.c.
98	P93	O	n.c.		L	n.c.
99	P94	O	n.c.		L	n.c.
100	P95	O	n.c.		L	n.c.

POS. NO	VERS. COLOR	PART NO. (FOR EUR)	DESCRIPTION	PART NO. (MJI)
001B	BLACK	05AK248010	FRONT PANEL AL BLACK	05AK248010
001B	GOLD	05AK248110	FRONT PANEL AL GOLD	05AK248110
002B		185J251010	MARANTZ BADGE BL PAINTED	185J251010
009B	BLACK	383K105020	FRONT CHASSIS PL RIGHT BLACK	383K105020
009B	GOLD	05AK105110	FRONT CHASSIS PL RIGHT GOLD	05AK105110
010B	BLACK	385K105010	FRONT CHASSIS PL LEFT BLACK	385K105010
010B	GOLD	05AK105210	FRONT CHASSIS PL LEFT GOLD	05AK105210
020B		05AK158010	WINDOW PINK SMOKE	05AK158010
023B		05AK270020	BUTTON 10 KEY	05AK270020
025B	BLACK	383K270040	BUTTON PLAY STOP BLACK	383K270040
025B	GOLD	05AK270140	BUTTON PLAY STOP GOLD	05AK270140
026B	BLACK	383K270020	BUTTON OPEN/CLOSE BLACK	383K270020
026B	GOLD	05AK270130	BUTTON OPEN/CLOSE GOLD	05AK270130
030B	BLACK	05AK063010	ESCUTCHEON BLACK	05AK063010
030B	GOLD	05AK063110	ESCUTCHEON GOLD	05AK063110
031B		05AK251010	BADGE ESCUTCHEON	05AK251010
037B	BLACK	386K154010	JOG KNOB BLACK	386K154010
037B	GOLD	05AK154110	JOG KNOB GOLD	05AK154110
048B	BLACK	284T154310	KNOB	284T154310
			HEADPHONE VOLUME BLACK	
048B	GOLD	284T154250	KNOB	284T154250
			HEADPHONE VOLUME GOLD	
050B	BLACK	05AK270010	BUTTON POWER BLACK	05AK270010
050B	GOLD	05AK270110	BUTTON POWER GOLD	05AK270110
009G		183J057010	LEG (GOLD HOT STAMP) FRONT	183J057010
010G		183J057110	LEG (GOLD HOT STAMP) REAR	183J057110
026G		376K121010	LINK POWER SW	376K121010
001M		05AK304500	MECHA LOADER ASSY VAL2212 08	05AK304500
002M		05AK304510	MECHA TRAVERSE ASSY VAM2202 08	05AK304510
L098		FC50270040	FERRITE CORE USB-4 TRANS-MAIN	FC50270040
L099		FC90280010	FERRITE CORE HF70SH28X2X10 SERVO-MAIN	FC90280010
▲ W001	/C	nsp	MAINS CORD KOREA 3A 250V	YC02000820
▲ W001	/F	nsp	MAINS CORD F HVFF 2X1.25	YC01800940
▲ W001	/K	nsp	MAINS CORD CCEE APP. (AC250V 10A)+ VAR2P	YC01800880
▲ W001	/N	YC01800790	MAINS CORD N (MAYOR)	YC01800790
▲ W001	/S	nsp	MAINS CORD BS WITH VAR-2	YC01800930
WF01		nsp	FFC (JF01-JY01)	YU20110520
WM01		nsp	FFC (J150-J103)	YU24140520
			PACKING	
001T	/F	nsp	USER GUIDE CD7300 F	05AK851110
001T	/K/S	nsp	USER GUIDE CD7300(ENG/CHI)	05AK851350
001T	/C	nsp	USER GUIDE CD7300(C)	05AK851210
001T	/N	05AK851250	USER GUIDE CD7300(N)	05AK851250
009T	/F	nsp	USER MANUAL ADDRESS SHEET	416K851130
WZ03		ZK05AK0010	REMOTE CONTROLLER RC7300CD	ZK05AK0010
			NOT STANDARD SPARE PARTS	
001D	BLACK	nsp	LID TOP COVER BLACK	349K257020
001D	GOLD	nsp	LID TOP COVER GOLD	349K257120
001S		nsp	PACKING CASE	05AK801010
002S		nsp	CUSHION	386K809010

NOTE : "nsp" PART IS LISTED FOR REFERENCE ONLY, MARANTZ WILL NOT SUPPLY THESE PARTS.



POS. NO	VERS. COLOR	PART NO. (FOR EUR)	DESCRIPTION	PART NO. (MJI)
1		QP40410986	SUSPENSION(4822 404 10986)	QP40410986
2		QP40111709	CLAMPER(4822 401 11709)	QP40111709
3		QP35810266	BELT(4822 358 10266)	QP35810266

NOTE : "nsp" PART IS LISTED FOR REFERENCE ONLY, MARANTZ WILL NOT SUPPLY THESE PARTS.

12. ELECTRICAL PARTS LIST

ASSIGNMENT OF COMMON PARTS CODES.

RESISTORS

R***: 1) GD05 × × × 140, Carbon film fixed resistor, ±5% 1/4W

R***: 2) GD05 × × × 160, Carbon film fixed resistor, ±5% 1/6W

① — Resistance value

Examples ;

① Resistance value

0.1 Ω 001	10 Ω 100	1 kΩ 102	100 kΩ 104
0.5 Ω 005	18 Ω 180	2.7 kΩ 272	680 kΩ 684
1 Ω 010	100 Ω 101	10 kΩ 103	1 MΩ 105
6.8 Ω 068	390 Ω 391	22 kΩ 223	4.7 MΩ 475

Note : Please distinguish 1/4W from 1/6W by the shape of parts used actually.

CAPACITORS

C***: CERAMIC CAP.

3) DD1 × × × × 370, Ceramic capacitor
Disc type
Temp.coeff.P350 ~ N1000, 50V
② — Capacity value
③ — Tolerance

Examples ;

② Tolerance (Capacity deviation)

±0.25 pF 0
±0.5 pF 1
±5% 5

* Tolerance of COMMON PARTS handled here are as follows :

0.5 pF ~ 5 pF ±0.25 pF
6 pF ~ 10 pF ±0.5 pF
12 pF ~ 560 pF ±5%

③ Capacity value

0.5 pF 005	3 pF 030	100 pF 101
1 pF 010	10 pF 100	220 pF 221
1.5 pF 015	47 pF 470	560 pF 561

C***: CERAMIC CAP.

4) DK16 × × × 300, High dielectric constant ceramic capacitor
Disc type
Temp.chara. 2B4, 50V
④ — Capacity value

Examples ;

④ Capacity value

100 pF 101	1000 pF 102	10000 pF 103
470 pF 471	2200 pF 222	

C***: 5) ELECTROLY CAP. (), 6) FILM CAP. ()

5) EA × × × × × 10, Electrolytic capacitor
One-way lead type, Tolerance ±20%
⑤ — Working voltage
⑥ — Capacity value

Examples ;

⑤ Capacity value

0.1 μF 104	4.7 μF 475	100 μF 107
0.33 μF 334	10 μF 106	330 μF 337
1 μF 105	22 μF 226	1100 μF 118
		2200 μF 228

⑥ Working voltage

6.3V 006	25V 025
10V 010	35V 035
16V 016	50V 050

6) DF15 × × × 350 — Plastic film capacitor
DF15 × × × 310 — One-way type, Mylar ±5% 50V
DF16 × × × 310 — Plastic film capacitor
One-way type, Mylar ±10% 50V
⑦ — Capacity value

Examples ;

⑦ Capacity value

0.001 μF (1000 pF) 102	0.1 μF 104
0.0018 μF 182	0.56 μF 564
0.01 μF 103	1 μF 105
0.015 μF 153	

NOTE : 1) The above CODES (R***, R***, C***, C*** and C***) are omitted on the schematic diagram in some case.

2) On the occasion, be confirmed the common parts on the parts list.

3) Refer to "Common Parts List" for the other common parts (R105, DD4, DK4).

NOTE ON SAFETY FOR FUSIBLE RESISTOR :

The suppliers and their type numbers of fusible resistors are as follows;

1. KOA Corporation

Part No. (MJI)	Type No. (KOA)	Description
NH05 × × × 140	RF25S × × × × ΩJ	(±5% 1/4W)
NH05 × × × 120	RF50S × × × × ΩJ	(±5% 1/2W)
NH85 × × × 110	RF73B2A × × × × ΩJ	(±5% 1/10W)
NH95 × × × 140	RF73B2E × × × × ΩJ	(±5% 1/4W)

* Resistance value (0.1 Ω – 10 kΩ)

2. Matsushita Electronic Components Co., Ltd

Part No. (MJI)	Type No. (MEC)	Description
NF05 × × × 140	ERD-2FCJ × × ×	(±5% 1/4W)
RF05 × × × 140		
NF02 × × × 140	ERD-2FCG × × ×	(±2% 1/4W)
RF02 × × × 140		

* Resistance value

Examples ;

* Resistance value

0.1 Ω 001	10 Ω 100	1 kΩ 102	100 kΩ 104
0.5 Ω 005	18 Ω 180	2.7 kΩ 272	680 kΩ 684
1 Ω 010	100 Ω 101	10 kΩ 103	1 MΩ 105
6.8 Ω 068	390 Ω 391	22 kΩ 223	4.7 MΩ 475

ABBREVIATION AND MARKS

ANT. : ANTENNA	BATT. : BATTERY
CAP. : CAPACITOR	CER. : CERAMIC
CONN. : CONNECTING	DIG. : DIGITAL
HP : HEADPHONE	MIC. : MICROPHONE
μ-PRO : MICROPROCESSOR	REC. : RECORDING
RES. : RESISTOR	SPK : SPEAKER
SW : SWITCH	TRANSF. : TRANSFORMER
TRIM. : TRIMMING	TRS. : TRANSISTOR
VAR. : VARIABLE	X'TAL : CRYSTAL

NOTE ON FUSE :

Regarding to all parts of parts code **FS20xxx2xx**, replace only with Wickmann-Werke GmbH, Type 372 non glass type fuse.

NOTE ON SAFETY :

Symbol  Fire or electrical shock hazard. Only original parts should be used to replaced any part marked with symbol  . Any other component substitution (other than original type), may increase risk of fire or electrical shock hazard.

安全上の注意 :

 がついている部品は、安全上重要な部品です。必ず指定されている部品番号の部品を使用して下さい。

POS. NO	VERS. COLOR	PART NO. (FOR EUR)	DESCRIPTION	PART NO. (MJI)
			PT01-TRANS CIRCUIT BOARD	
▲ J007	/K	nsp	PLUG B3P5-VH	YP04000920
▲ J007	/C/F/N /S	YP04000760	PLUG CONNECTOR 2P B3P-VH	YP04000760
▲ T001	/F	nsp	MAINS TRANSF. EI57-25 100V 50/60HZ	TS15725190
▲ T001	/K	nsp	MAINS TRANSF. EI57-25 110V/220V 50/60HZ	TS15725200
▲ T001	/C/N/S	TS15725210	MAINS TRANSF. EI57-25 230V 50HZ	TS15725210

NOTE : "nsp" PART IS LISTED FOR REFERENCE ONLY, MARANTZ WILL NOT SUPPLY THESE PARTS.